



EC-01G-Kit Specification

Version V2.0.1

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Resumed

Version	Date	Develop/revise content	Formulate	check and
V1.0.0	2021.09.17	For the first time, formulate	ShiHai Mo	Hong Xu
V2.0.1	2021.09.23	Modified Indicator description and specifications template	ShiHai Mo	Hong Xu

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1. Product Overview

EC-01G-Kit is a development board designed for The EC-01G module, which is shared with the EC-01-Kit. The development board integrates CH340C serial port chip and SIM card holder to facilitate the use of developers.

EC-01G is a NB+GPS module developed by Ai-Thinker. The main chip scheme adopted in NB part is EC616S. The NB-IoT chip features ultra-integrated NB-IoT soCs, ultra-low power consumption and full support for the 3GPP Rel14 NB-IoT standard, making it an extremely cost-effective NB-IoT chip.

It has the following characteristics (NB-IoT) :

- Integrated RF transceiver, PA, RF filter, antenna switch and power management
- Excellent communication performance and stability in a variety of wireless environments
- Excellent power consumption performance in various modes (PSM, DRX, eDRX, connected state)
- Unique MCU mode, providing lower operating current and shorter wake up time

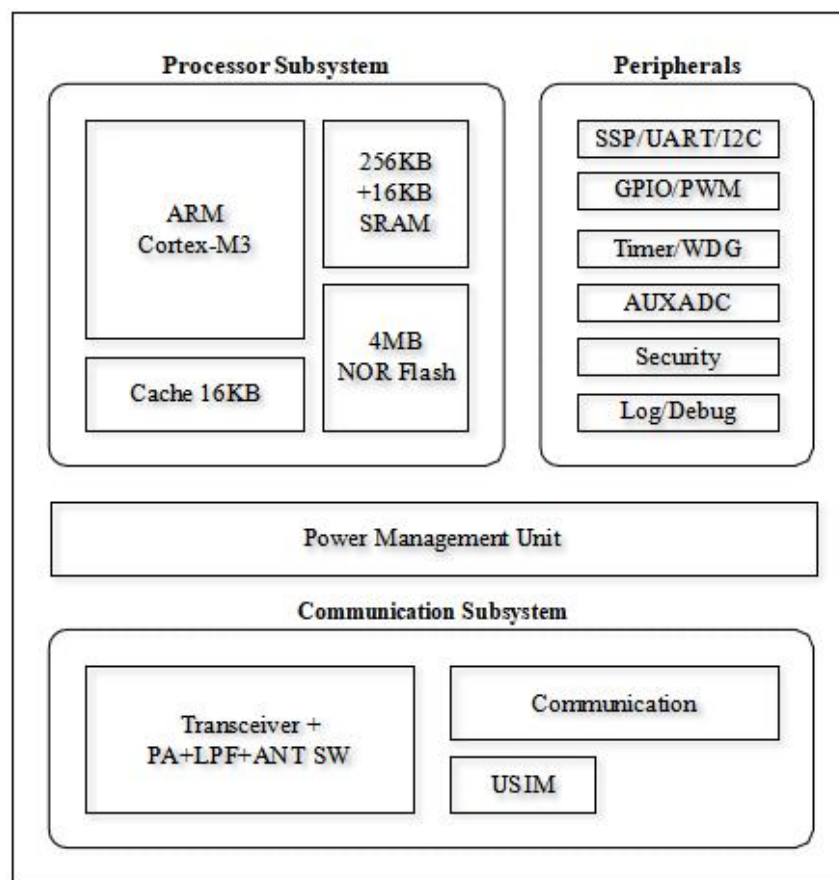


Figure 1 EC616S chip architecture diagram

The GPS part of EC-01G module adopts AT6558R as the main chip scheme. The chip is a high-performance BDS/GNSS multi-mode satellite navigation receiver SoC single chip, on-chip integrated RF front end, digital baseband processor, 32-bit RISC CPU, power management function. The chip supports a variety of satellite navigation systems, including China's Beidou Navigation Satellite System BDS, GPS of the United States, and GLONASS of Russia, and can realize joint positioning of multiple systems.

It has the following characteristics (GPS) :

- BDS/GPS/GLONASS multi-system combined positioning and single system independent positioning are supported
- With active antenna detection and protection
- RTC and backup circuit power supply can be as low as 1.4V
- BDS/GPS dual mode continuous operation

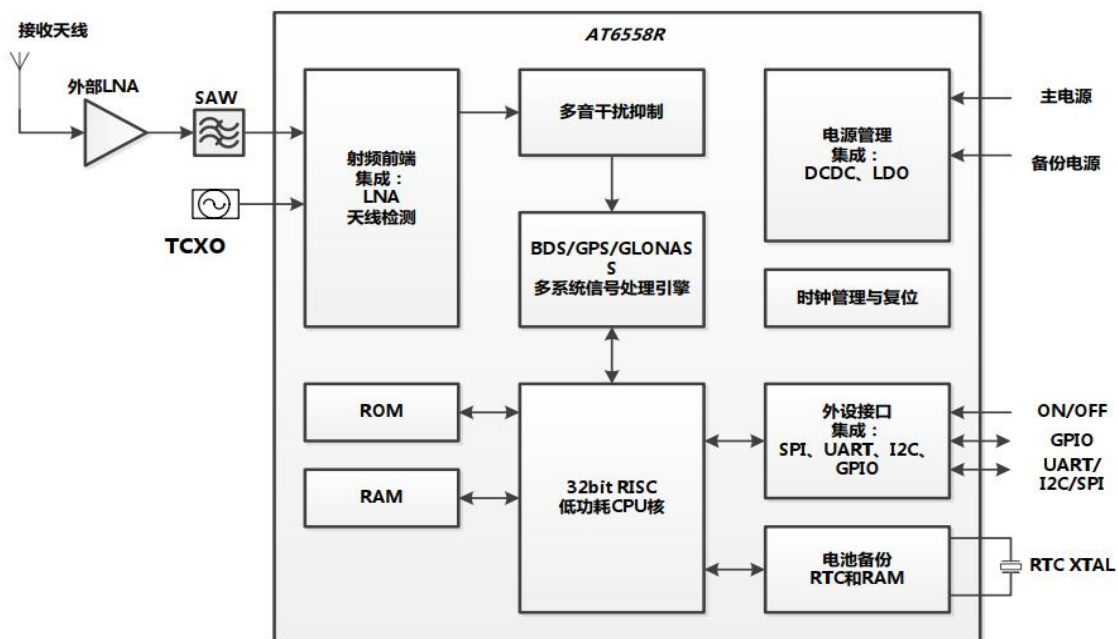


Figure 2 AT6558R chip architecture diagram

1.1. Characteristics

- Processor :
 - ✓ Cortex-M3, support MPU
 - ✓ Configurable CPU frequency, Max 204MHz
 - ✓ 8-Channel DMA
- Storer :

- ✓ 4MB NOR Flash on chip
- ✓ 272KB WIW SRAM, divided into 256KB and 16KB
- ✓ 16KB instruct cache
- system
- ✓ Flexible Configuration Support 1.8/2.8/3.3V IO
- ✓ Clock source: 26MHz TCXO or DCXO, 32.768khz crystal oscillator
- ✓ 1 external wake up source (interrupt)
- ✓ Unique MCU mode, in this mode, internal RC oscillator as the clock, lower power consumption
- ✓ LOG □, UNILog
- ✓ Debug port, SWD
- Peripheral
- ✓ 12 GPIO(Include TX0\RX0 AND TX1\RX1)
- ✓ 2 UART, 2 ADC, 1 I2C
- ✓ 6 PWM, 6 Timers, 6 GPIO counter, 1 WDG
- ✓ 32KHz RTC timer
- ✓ USIM, Support Esim
- ✓ 1 SIM Booth
- low power consumption:
- ✓ Unique low power architecture, level 4 sleep modePSM: 800nA
- ✓ DRX (2.56s): typical value110uA
- ✓ RX:typical value10mA
- ✓ TX:typical value 24mA
- Communication:
- ✓ Fully support 3GPP R14 NB-IoT
- ✓ Category NB2, 2-HARQ
- ✓ Multi-tone NPUSCH
- ✓ Anchor and non-anchor carrier
- ✓ In-band same/different PCI, guardband, standalone
- ✓ Multi-carrier paging, NPRACH
- ✓ Positioning: OTDOA & ECID
- ✓ ROHC, RAI, multiple-DRB, RRC connection re-establish
- ✓ SC-PTM (need SW upgrade)

- Radio Frequency:
 - ✓ Support the spectrum: Band3、Band5、Band8
 - ✓ chip integrates PA and supports APT function
 - ✓ chip integrated RF transceiver filter and antenna switch
 - ✓ Power level 3
- Safety:
 - ✓ Hardware Encryption and Decryption module (AES, SHA) Secure boot
 - ✓ Flash encryption
 - ✓ True random number generator
- Application:
 - ✓ Support Open-CPU
 - ✓ software conforms to the CMSIS architecture
 - ✓ Supports mainstream cloud services
 - ✓ IPv4, IPv6 and non-IP
 - ✓ UDP, TCP
 - ✓ DTLS, TLS, SSL
 - ✓ MQTT, CoAP, HTTP(S)
 - ✓ LWM2M
 - ✓ Support FOTA
- Power Supply : 5V

2. Main parameters

Table 1 Main parameters

Development board Model	EC-01G-Kit
Adaptive pattern combination	EC-01、EC-01G
Packaging	DIP-14 (2.54Spacing standard row needle)
Size	37.6*32.5(±0.2)mm
Antenna form	NB:External SMA antenna, GPS: external IPEX antenna
Spectral Range	Band3, Band5, Band8
Working Temperature	-40 °C ~ 85 °C
Storage Environment	-40 °C ~ 125 °C , < 90%RH

Supply District	Power supply voltage 5V current greater than 500mA
Support interface	UART/I2C/PWM/ADC/GPIO
IO quantity	12 (contain TX0\RX0 TX1\RX1)
Series Rate	Support 110 ~ 4608000 bps , Default 9600 bps
Bluetooth	Nonsupport
Security	AES/SHA
Flash	4MB NOR Flash
Module Power Consumption	Sleep mode (PSM) : 0.8uA (Single Module) Standby mode (DRX) : 110uA (Single Module) Full load mode (TX: 23dBm) : 240mA (Single Module)

2.1. Power supply selection

You can power the EC-01G-Kit using either of the following power supply methods:

- Micro-USB Interface power supply (default)
- 3V3 and GND Fixing pin power supply

Suggest to use the first power supply mode: Micro-USB port

2.2. Electrostatic requirements

EC-01G-Kit Development boards are electrostatic sensitive equipment and special precautions should be taken when handling them.



Figure 3 ESD preventive measures

2.3. Electrical characteristics

Table 2 Electrical characteristics

parameter	condition	minimum	typical value	maximum	unit	
supply voltage	VDD	4.5	5	5.3	V	
I/O	V_{IL}/V_{IH}	-	-0.3/0.75V _{I0}	-	0.25V _{I0} /4.5	V
	V_{OL}/V_{OH}	-	N/0.8V _{I0}	-	0.1V _{I0} /N	V
	I_{MAX}	-	-	-	24	mA

2.4. NB-IoT Rf performance

Table 3 NB-iot RF performance table

Band	Channel	1 Tone@11 (15KHz)				12 Tone (15KHz)			
		Pout (dBm)	EVM RMS (%)	SEM Margin (dB)	ACLR Max (dBc)	Pout (dBm)	EVM RMS (%)	SEM Margin (dB)	ACLR Max (dBc)
3	1201	22.5	0.9	4.9	-39.5	20.5	7	6	-40.8
	1575	22.5	0.9	3.8	-39	20.5	7	6	-41
	1949	22.5	0.9	4	-39	20.5	7	5	-40.5
5	2401	22.6	0.9	8	-42	20.4	7	7	-43
	2525	22.6	0.9	9	-42	20.4	6	6	-42.5
	2649	22.6	0.9	8	-42	20.4	7	7	-42.8
8	3451	22.5	0.9	7.5	-42.5	20.5	6	4	-42.5
	3625	22.5	0.9	8.5	-42	20.4	6	3.5	-41
	3799	22.5	0.9	5	-42	20.4	7	4.5	-40.5

2.5. Module power consumption

The following power consumption figures are based on a 3.3V power supply, ambient temperature at 25° C, and using an internal voltage regulator.

Table 4 Module power consumption table

Model	minimum	average value	maximum	Unit
Connect_Tx_23dBm_1Tone (Band3 Channel 1575 1842.5MHz)	-	120	240	mA
Connect_Tx_23dBm_1Tone (Band5 Channel 2525 881.5MHz)	-	110	226	mA
Connect_Tx_23dBm_1Tone (Band8 Channel 2625 942.5MHz)	-	108	215	mA
Connect_Rx_Band3	-	10	40	mA
Connect_Rx_Band5	-	16	46	mA
Connect_Rx_Band8	-	10	40	mA
DRX (2.56s)	-		110	μ A
PSM	-		<1	μ A

3. External dimensions

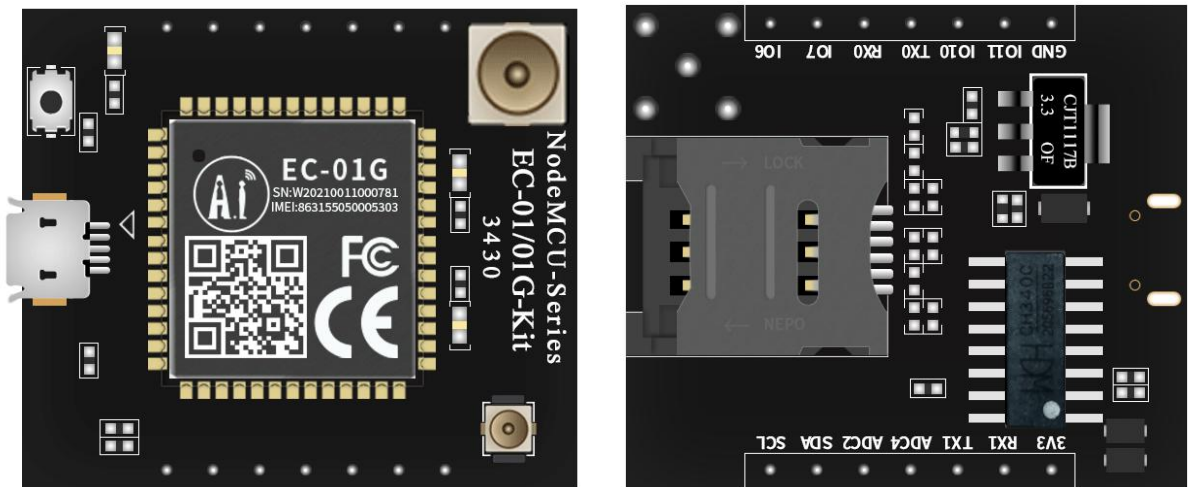


Figure 4 Appearance of development board (picture and silkscreen are for reference only, subject to the real object)

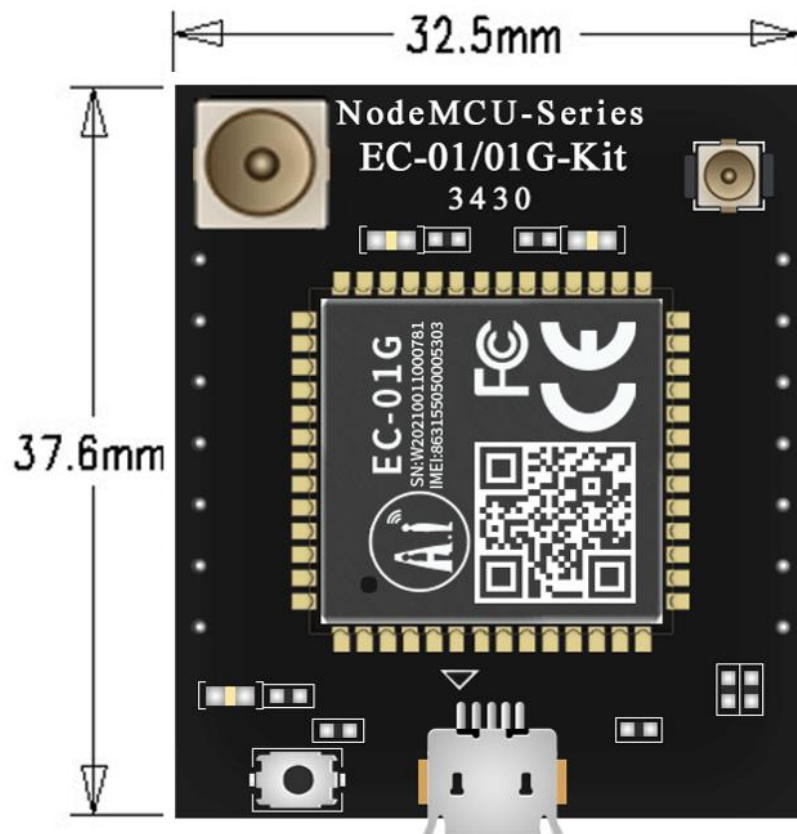


Figure 5 Development board size diagram

4. Description of indicators and buttons

EC-01G-Kit there are three indicators, were “NB” indicator light, “GPS” indicator light and “LINK” indicator light.

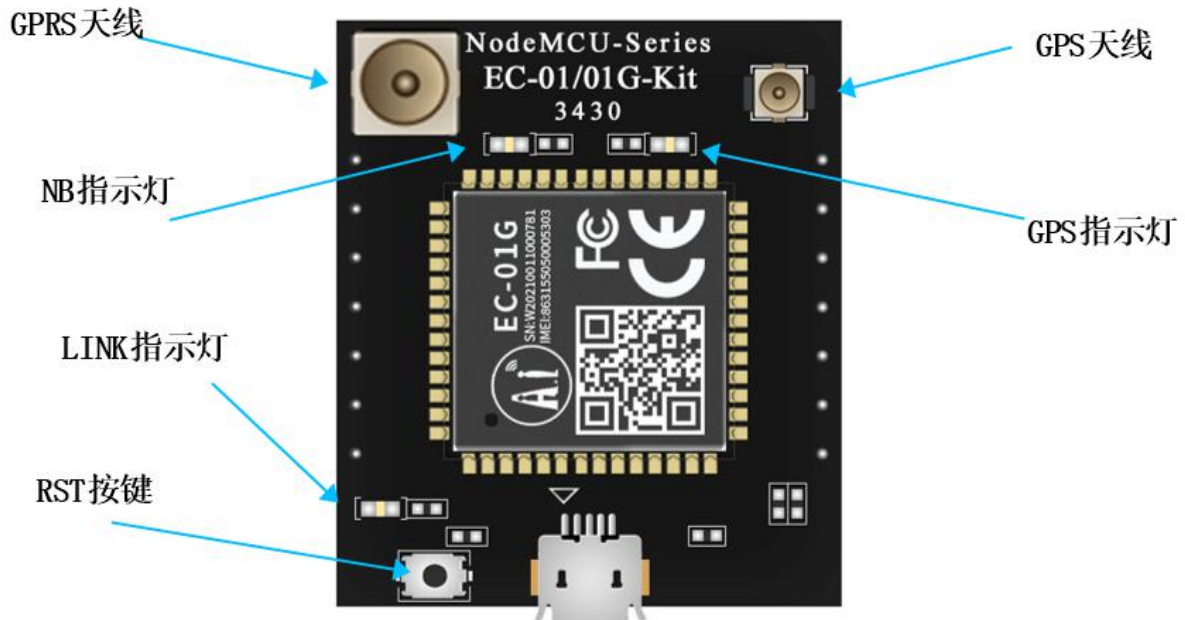


Figure 6 POSITIONS of the INDICATORS and buttons on the EC-01G-KIT

Table 5 Indicator status and button functions

Indicator light or button	Network state	LED status or Button Function	Remark
NB” Indicator (Network status indicator)	Connected	1s Flashing once	Turn on the indicator mode (AT+EGLEDMODE=1)
	Search the Internet	0.5s Flashing once (quick flashing)	
	Network disconnection	Extinguish	
	Ununited	0.5s Flashing once (quick flashing)	
“GPS”	Not open	Extinguish	/

Indicator			
	Enabled (Not located)	Slow flash	/
	Has been positioning	Light-Form	/
“LINK” Indicator	Failed to connect to server	Extinguish	/
	Connected Server	Light-Form	Connect to server and ID=0
RST Key	/	Resetting Key	/

5. Pin definition

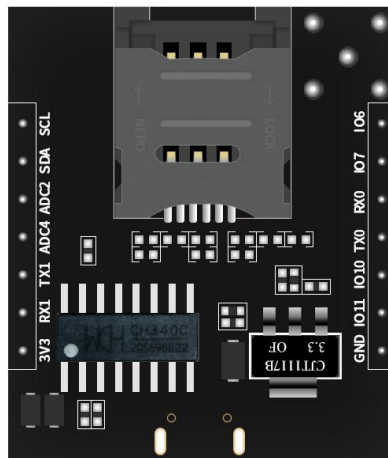


Figure 7 Schematic diagram of development board pins (bottom view)

EC-01G-Kit A total of 14 interfaces are connected, such as pin schematic diagram, pin function definition table is the interface definition.

Table 6 Pin function definition table

Foot sequence	Name	Function Declaration
1	IO6	GP106
2	IO7	GP107
3	RX0	GP108/UART0_RXD

4	TX0	GPI09/UART0_TXD
5	I010	GPI010
6	I011	GPI011
7	GND	Ground Connection
8	3V3	3.3V Power Supply
9	RX1	GPI014/UART1_RXD
10	TX1	GPI015/UART1_TXD
11	ADC4	ADC Channel AI04
12	ADC2	ADC Channel AI02
13	SDA	GPI02
14	SCL	GPI03

6. Schematic diagram

EC-01G-Kit and EC-01-Kit share a schematic.

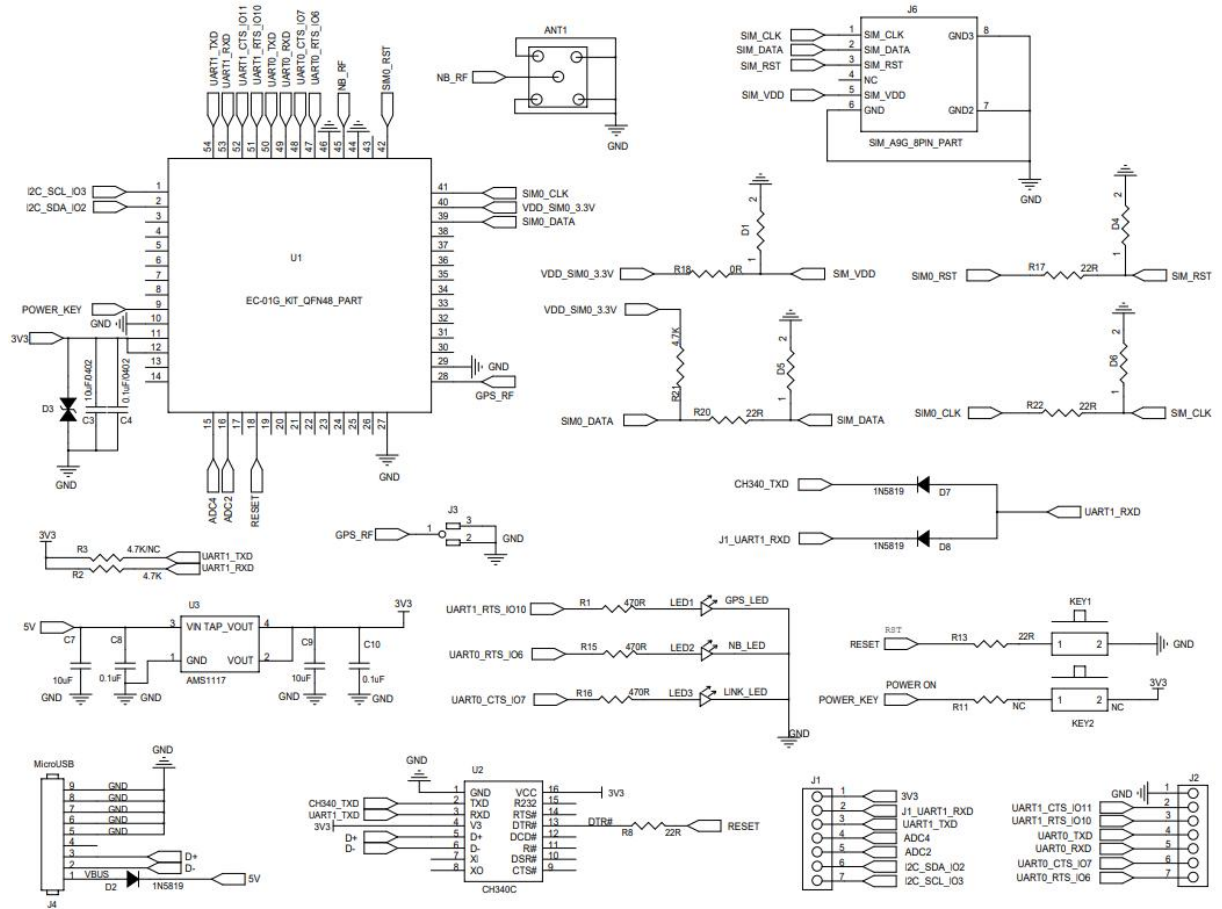


Figure 8 Schematic diagram of development board

7. Product model

Table 7 Product model table

Product Model	Power Supply	Pin TX1 and RX1	Package	Size	GPS
EC-01 Module	3.3V ~ 4.5V, I>500mA	/	SMD-54	19.2*18.8*2.8 (±0.2)MM	No
EC-01G Module	3.3V ~ 4.5V, I>500mA	/	SMD-54	19.2*18.8*2.8 (±0.2)MM	Yes
EC-01F Module	3.3V ~ 4.5V, I>500mA	/	SMD-44	17.7*15.8*2.8 (±0.2)MM	No
EC-01-Kit_V1.0 Development Board	5V, I>500mA	Access not support	DIP-14	37.6*32.5 (±0.2) mm	No
EC-01G-Kit_V1.0 Development Board	5V, I>500mA	Access not support	DIP-14	37.6*32.5 (±0.2) mm	Yes
EC-01F-Kit_V1.0 Development Board	5V, I>500mA	Access not support	DIP-14	33.9*28.4*13 (±0.2)mm	No
EC-01-Kit_V2.0 Development Board	5V, I>500mA	Support access	DIP-14	37.6*32.5 (±0.2) mm	No
EC-01G-Kit_V2.0 Development Board	5V, I>500mA	Support access	DIP-14	37.6*32.5 (±0.2) mm	Yes
EC-01F-Kit_V2.0 Development Board	5V, I>500mA	Support access	DIP-14	33.9*28.4*13 (±0.2)mm	No
Product Information: https://docs.ai-thinker.com/nb_module					

8. Product notes

EC-01G-kit V1.0 can only use one of the onboard micro-USB ports, TX1 and RX1 for communication and recording. When the serial port chip as shown in the picture exists, only onboard micro-USB port can be used for communication and recording. If you want to use TX1 and RX1 pin row for communication and burning, you need to remove the serial chip, at this time micro-USB port does not support communication and burning. Figure 10 shows the differences between EC-01G-Kit V1.0 and EC-01G-Kit V2.0

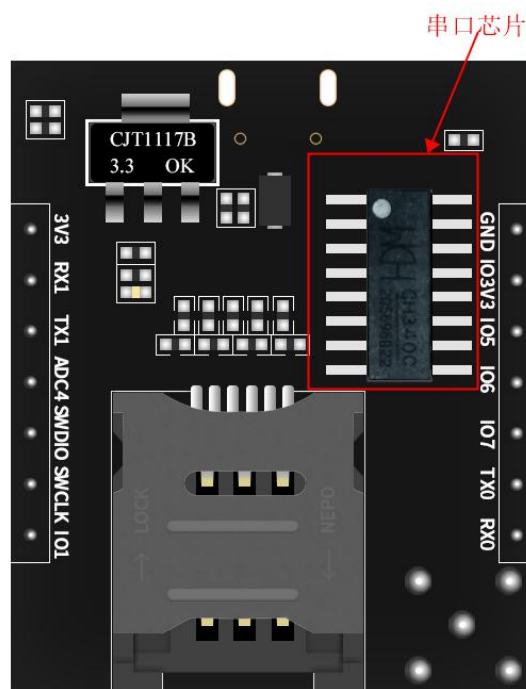


Figure 9 schematic diagram of serial port chip

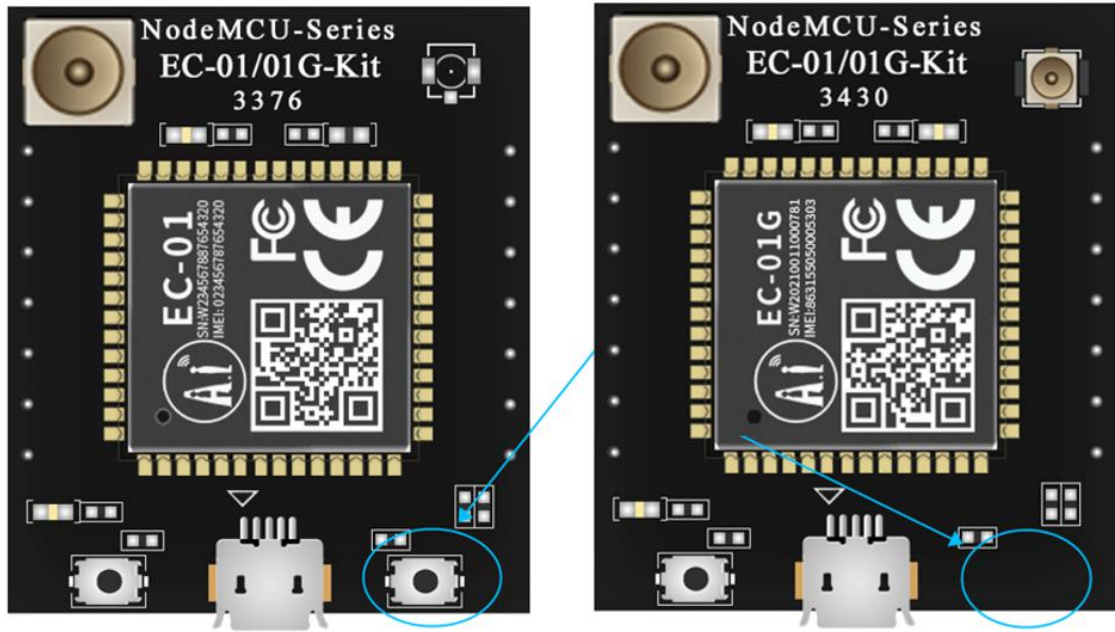


Figure 10 Difference between EC-01G-Kit V1.0(left) and EC-01G-Kit V2.0(right)

9. Product packaging information

As shown in the figure below, the EC-01G-kit development board is packed in electrostatic bags, 1pcs /bag.



Figure 11 Packing diagram of an ESD bag

10. Contact us

Official website: <https://www.ai-thinker.com>

Develop Wiki: <https://docs.ai-thinker.com>

Official forum: <http://bbs.ai-thinker.com>

Sample purchase: <https://aithinker.tmall.com>

<https://anxinke.taobao.com>

Business cooperation: sales@aithinker.com

Technical support: support@aithinker.com

Company Address: 7/F, Fengze Building B, Huafeng Industrial Park 2th, Hangkong street, Xixiang Raod, Baoan, Shenzhen China.

Telephone: 0755-29162996

