

# Ai-M62-32S-Kit Specification

Version V1.0.1

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### **Document resume**

Version	Date	Develop/revise content	Edition	Approve
V1.0.0	2023.03.24	First Edition	Cong Chen	Hong Xu
V1.0.1	2023.11.15	<ol> <li>Update the chip block diagram and the supported peripheral interfaces;</li> <li>Add important statements</li> </ol>	Fangke Huang	Hong Xu



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#### 1. Product Overview

The Ai-M62-32S-Kit is a development board designed for the Ai-M62-32S module. Ai-M62-32S is a Wi-Fi 6 + BLE 5.3 module developed by Shenzhen Ai-Thinker Technology Co., Ltd. The module is equipped with BL616 chip as the core processor, supports Wi-Fi 802.11b/g/n/ax protocol and BLE protocol, and supports Thread protocol. The BL616 system contains a low-power 32-bit RISC-V CPU with floating point cells, DSP units, cache, and memory, up to 320M.

Ai-M62-32S module has rich peripheral interfaces, including USB2.0, SDU, SD / MMC (SDH), SPI, UART, I2C, I2S, PWM, GPDAC, GPADC, ACOMP and GPIO, etc. It can be widely used in audio and video multimedia, Internet of Things (IoT), mobile devices, wearable electronic devices, smart home and other fields.

Ai-M62-32S module Sec Eng module supports AES / SHA / PKA / TRNG and other functions, supports mirror encryption and signature startup, to meet the needs of various security applications in the field of the Internet of Things.

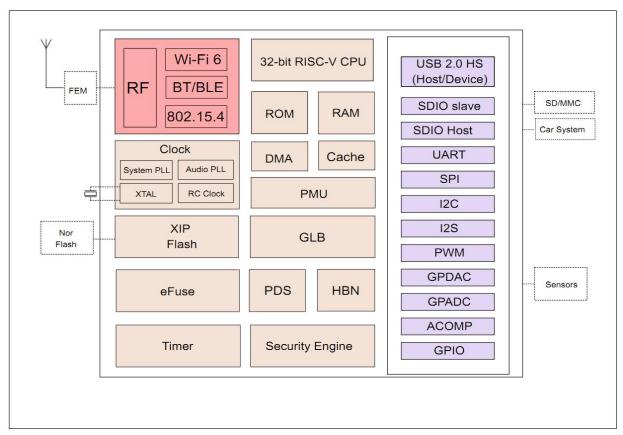


Figure 1 Main chip block diagram



#### 1.1. Characteristic

- The package is DIP-38
- Support 2.4GHz frequency
- Support IEEE 802.11 b/g/n/ax
- Support BLE5.3
- Support Thread
- Support Wi-Fi/BLE/Thread coexistence
- Wi-Fi security support WPS/WEP/WPA/WPA2/WPA3
- Support 20/40MHz bandwidth ,1T1R,speed up to 229.4 Mbps
- Support STA、SoftAP、STA+SoftAP and sniffer mode
- A 32-bit RISC-VCPU with FPU and DSP, with a maximum main frequency of up to 320M
- 532KB SRAM,128KB ROM,4Kb eFuse
- Support USB2.0, SDU, SD / MMC (SDH), SPI, UART, I2C, I2S, PWM, GPDAC, GPADC, ACOMP and GPIO, etc
- Integrated RF Balun、PA/LNA
- Support safe startup; safe debugging
- Support XIP QSPI On-The-Fly AES deciphering (OTFAD)
- Support TrustZone
- Support AES-CBC/CCM/GCM/XTS mode
- Support MD5、SHA-1/224/256/384/512
- Support TRNG (True random number generator)
- Support PKA for RSA / ECC (Public key accelerator)
- Support the Wi-Fi fast connection for BLE
- General AT instructions can be quickly used
- Support for secondary development, with integrated Windows, Linux development environments



# 2. Main parameters

Table 1 Description of the main parameters

Development board model	Ai-M62-32S-Kit	
Development board package	DIP-38	
Size	54.96*25.40(±0.2)mm	
Antenna	on-board PCB antenna	
Frequency	2400 ~ 2483.5MHz	
Operating temperature	-40℃ ~85℃	
Storage temperature	-40°C ~ 125°C, < 90%RH	
Power supply	Support voltage 3.3V or 5V, supply current ≥500mA	
Interface	USB2.0, SDU, SD / MMC (SDH), SPI, UART, I2C, I2S, PWM, GPDAC, GPADC, ACOMP and GPIO, etc	
Ю	18	
<b>UART</b> rate	Default 115200 bps	
Security	WPS/WEP/WPA/WPA2/WPA3	
Flash	Default 4MByte ,maximum support 16MByte	

#### 2.1. Power selection

Ai-M62-32S-Kit three power supply modes are supported:

- Type-C interface power supply (recommend)
- 5V and GND pin header power supply
- 3V3 and GND pin header power supply

### 2.2. Static electricity requirement

Ai-M62-32S-Kit is an electrostatic sensitive device. Therefore, you need to take special precautions when carrying it.



Figure 2 ESD preventive measures



### 2.3. Electrical characteristic

**Table 2 Electrical characteristics table** 

P	arameters	Condition	Min.	Typical value	Max.	Unit
Voltage		VDD	4.5	5	5.3	V
Voltage Supply		VDD	2.97	3.3	3.6	V
	VIL	-	-	-	0.3*VDDIO	V
	VIH	-	0.7*VDDIO	-	-	V
I/O	VOL	-	-	0.1*VDDIO	-	V
	VOH	-	-	0.9*VDDIO	-	V
	IMAX	-	-	-	15	mA

### 2.4. Wi-Fi RF Performance

Table 3 Wi-Fi RF Performance table

Description Typical value				Unit
Frequency range	24	2400 ~ 2483.5MHz		
	Output pov	ver		
Mode	Min.	Typical value	Max.	Unit
11ax Mode HE40, PA output power	-	16	-	dBm
11ax Mode HE20, PA output power	-	17	-	dBm
11n Mode HT40, PA output power	-	19	-	dBm
11n Mode HT20, PA output power	-	19	-	dBm
11g Mode, PA output power	-	19	-	dBm
11b Mode, PA output power	- 22 -		dBm	
	Receive Sensi	tivity		
Mode	Min.	Typical value	Max.	Unit
11b,1 Mbps	-	-98	-	dBm
11b,11 Mbps	-	-90	-	dBm
11g, 6 Mbps	-	-93	-	dBm
11g,54 Mbps	-	-76	-	dBm
11n, HT20 (MCS7)	-	-73	-	dBm
11ax, HE20 (MCS9)	-	-70	-	dBm
11ax, HE40 (MCS9)	-	-67	-	dBm



### 2.5. BLE RF Performance

Table 4 BLE RF performance table

Description	Typical value			Unit	
Frequency range		2400 - 2483.5			
	<b>Output Power</b>	•			
Rate Mode	Min.	Min. Typical value Max.			
1Mbps	-	10	15	dBm	
2Mbps	- 10 15		15	dBm	
Re	Receive Sensitivity				
Rate Mode	Min.	Typical value	Max.	Unit	
1Mbps sensitivity@30.8%PER	-	-99	-	dBm	
2Mbps sensitivity@30.8%PER	-	-97	-	dBm	

## 2.6. Power consumption

The following power consumption data are based on a 3.3V power supply, 25°C ambient temperature, and measured using an internal voltage regulator.

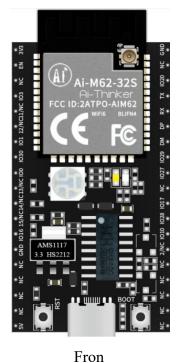
- All measurements are made at the antenna interface with a filter.
- All transmission data are based on 100% duty cycle in continuous transmission mode.

**Table 5 Power consumption table** 

Mode	Min.	AVG	Max.	Unit
Tx 802.11b, 11Mbps, POUT=+22dBm	-	442	-	mA
Tx 802.11g, 54Mbps, POUT =+19dBm	-	296	-	mA
Tx 802.11n, MCS7, POUT =+19dBm	-	301	-	mA
Tx 802.11ax, MCS9, POUT =+17dBm	-	269	-	mA
Rx 802.11b, packet length 1024 byte	-	59	-	mA
Rx 802.11g, packet length 1024 byte	-	59	-	mA
Rx 802.11n, packet length 1024 byte	-	59	-	mA
Rx 802.11ax, packet length 1024 byte	-	59	-	mA



# 3. Appearance Dimensions





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Figure 3 Appearance diagram (Rendering figure is for reference only, subject to physical objects)

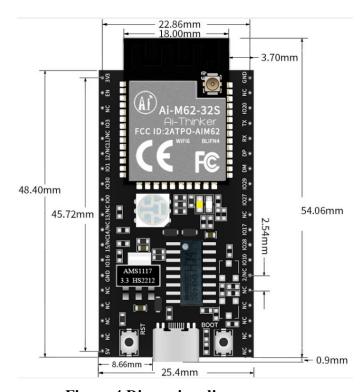


Figure 4 Dimension diagram



# 4. Description of the indicator light and the key button



Figure 5 Ai-M62-32S-Kit indicator light and key position Table 6 Ai-M62-32S-Kit indicator light and key position

1	RGB light (red light connect to IO1,green light connect IO30,blue light connect to IO0)
2	Reset buttom
3	White light (IO 29)
4	Warm light (IO 27)
⑤	Burning key. When burning, it is necessary to press the burn key and reset key successively, and then release the reset button and burn key successively



### 5. Pin definition

Ai-M62-32S-Kit connects with 38 interfaces, as shown in the schematic diagram of the pin, and the pin function definition table is the interface definition.

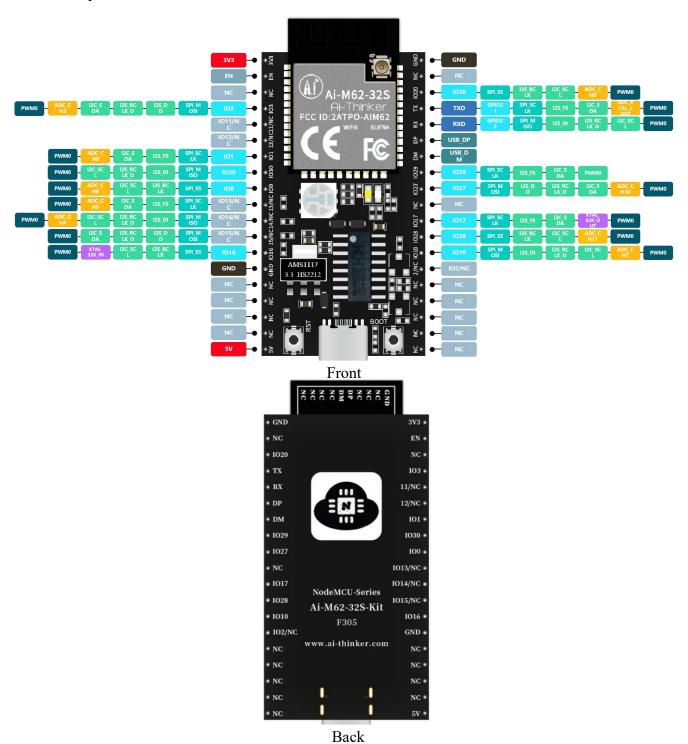


Figure 6 Schematic diagram of module pins



### **Table 7 Pin function definition table**

No.	Name	Function
1	3V3	3.3V power supply
2	EN	Chip enabled pin, high level effective
3	NC	NC
4	IO3	GPIO3/SPI_MOSI/I2S_DO/I2S_RCLK_O/I2C_SDA/ADC_CH3/PWM0
5	11/NC	The default is available, and the IO port is shared with Flash inside the module. If the external Flash module is customized, the IO is not usable. If you need to use it, please contact Ai-Thinker GPIO11/SPI_MOSI/I2S_DO/I2S_RCLK_O/I2C_SDA/PWM0
6	12/NC	The default is available, and the IO port is shared with Flash inside the module. If the external Flash module is customized, the IO is not usable. If you need to use it, please contact Ai-Thinker GPIO11/SPI_MOSI/I2S_DO/I2S_RCLK_O/I2C_SDA/PWM0
7	IO1	GPIO1/SPI_SCLK/I2S_FS/I2C_SDA/ADC_CH8/PWM0
8	IO30	GPIO30/SPI_MISO/I2S_DI/I2S_RCLK_O/I2C_SCL/PWM0
9	IO0	GPIO0/SPI_SS/I2S_BCLK/I2C_SCL/ADC_CH9/PWM0
10	IO13/NC	GPIO13/SPI_SCLK/I2S_FS/I2C_SDA/ADC_CH5/PWM0
11	IO14/NC	The default is available, and the IO port is shared with Flash inside the module. If the external Flash module is customized, the IO is not usable. If you need to use it, please contact Ai-Thinker GPIO15/SPI_MOSI/I2S_DO/I2S_RCLK_O/I2C_SDA/PWM0
12	IO15/NC	The default is available, and the IO port is shared with Flash inside the module. If the external Flash module is customized, the IO is not usable. If you need to use it, please contact Ai-Thinker GPIO15/SPI_MOSI/I2S_DO/I2S_RCLK_O/I2C_SDA/PWM0
13	IO16	GPIO16/SPI_SS/I2S_BCLK/I2C_SCL/XTAL_32K_IN/PWM0
14	GND	Ground
15	NC	NC
16	NC	NC
17	NC	NC
18	NC	NC
19	5V	5V power supply
20	NC	NC
21	NC	NC
22	NC	NC



23	NC	NC
24	NC	NC
25	IO2/NC	Default NC, is not available
26	IO10	GPIO10/SPI_MISO/I2S_DI/I2S_RCLK_O/I2C_SCL/ADC_CH7/PWM0
27	IO28	GPIO28/SPI_SS/I2S_BCLK/I2C_SCL/ADC_CH11/PWM0
28	IO17	GPIO17/SPI_SCLK/I2S_FS/I2C_SDA/XTAL_32K_OUT/PWM0
29	NC	NC
30	IO27	GPIO27/SPI_MOSI/I2S_DO/I2S_RCLK_O/I2C_SDA/ADC_CH10/PW M0
31	IO29	GPIO29/SPI_SCLK/I2S_FS/I2C_SDA/PWM0
32	DM	USB_DM
33	DP	USB_DP
34	RX	RXD/GPIO22/SPI_MISO/I2S_DI/I2S_RCLK_O/I2C_SCL/PWM0
35	TX	TXD/GPIO21/SPI_SCLK/I2S_FS/I2C_SDA/ADC_RCAL_VOUT/PW M0
36	IO20	GPIO20/SPI_SS/I2S_BCLK/I2C_SCL/ADC_CH0/PWM0
37	NC	NC
38	GND	Ground



# 6. Schematic diagram

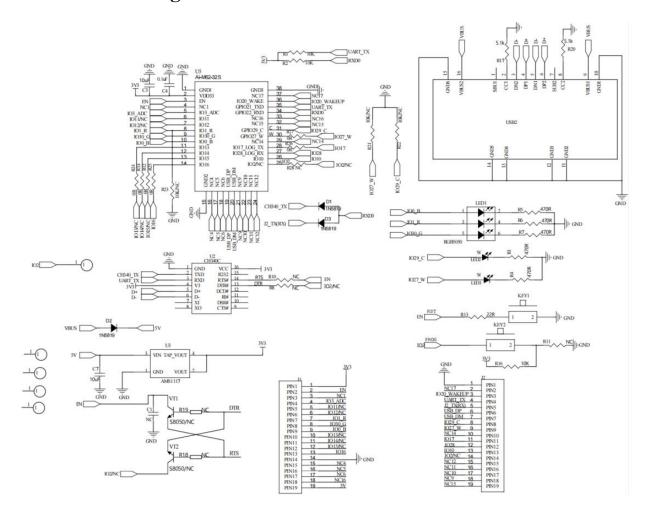


Figure 7 Module schematic



# 7. Product packing information

**Table 8 Packing information table** 

Packing list	Manner of packing	MOQ (Electrostatic bag)	SPQ (sealing bag)
Ai-M62-32S-Kit	Bubble cotton + electrostatic bag	1pcs	20pcs

### 8. Contact us

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